

Enumerating Pattern Matches in Words and Trees

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Problem: Finding patterns in text

- We have a **long text** *T*:

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→ **How to find the pattern P efficiently in the text T ?**

Solution: automata

- Convert the pattern from a **regular expression** to an **automaton**

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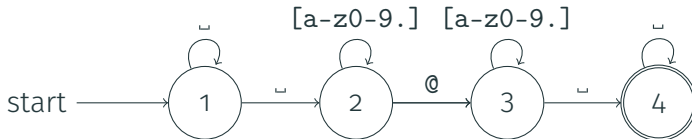
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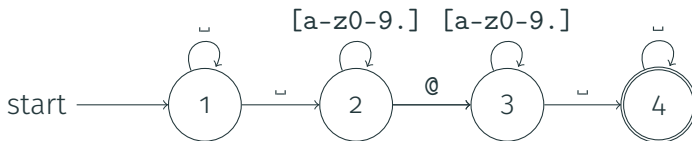
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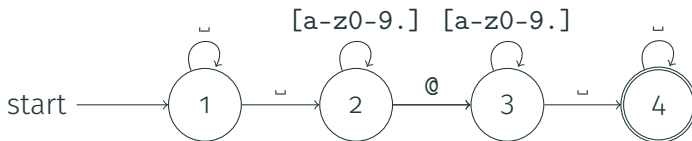
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- How **efficient** is this?

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→ Find all **pairs of positions** that are the endpoints of a match
- **Generalization:** patterns that can capture a **tuple** of positions
→ Find the email addresses **without leading/trailing spaces**
→ Find all **pairs** of a name followed by an email address

Patterns with capture variables

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→ **One match:** $\langle \alpha : 20, \beta : 32 \rangle$

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- We measure the complexity of the problem:

- In data complexity, as a function of T
 - In combined complexity, as a function of P and T

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- **Naive algorithm:** Consider **all ways** to assign capture variables and **test** for each of them if it satisfies the pattern

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→ We need a **different way** to measure complexity

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→ Formalization: **enumeration algorithms**

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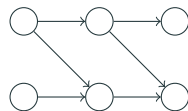
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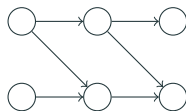
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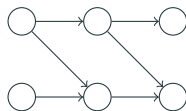
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Text T

Phase 1:
Preprocessing



Data structure

Phase 2:
Enumeration

$\{\langle \alpha : 42, \beta : 57 \rangle,$

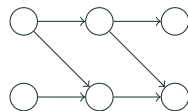
Results

Formalizing enumeration algorithms

Antoine Amarilli: Description Name Antoine
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1990-02-07. French national. Appearance as
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Pattern P

0011



State

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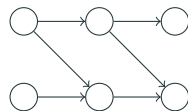
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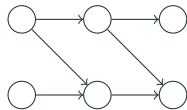
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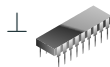
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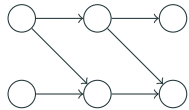
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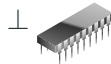
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Two ways to measure performance:

- Total time for phase 1
 - Delay between two results in phase 2
- ... in combined and data complexity

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- Can we do **better**?

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We can find all matches of a regexp with captures P on text T with:

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Automaton formalism

- We use automata that read letters and capture variables

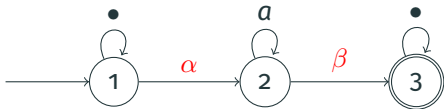
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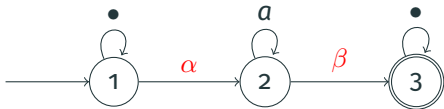
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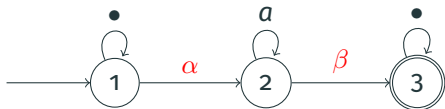


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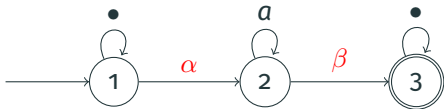


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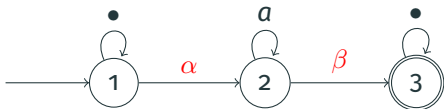


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- Challenge:** Because of **nondeterminism** we can have many different runs of **A** producing the same tuple!

Proof idea: product DAG

Compute a **product DAG** of the text T and of the automaton A

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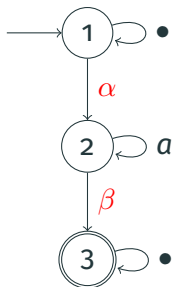
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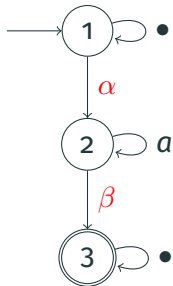


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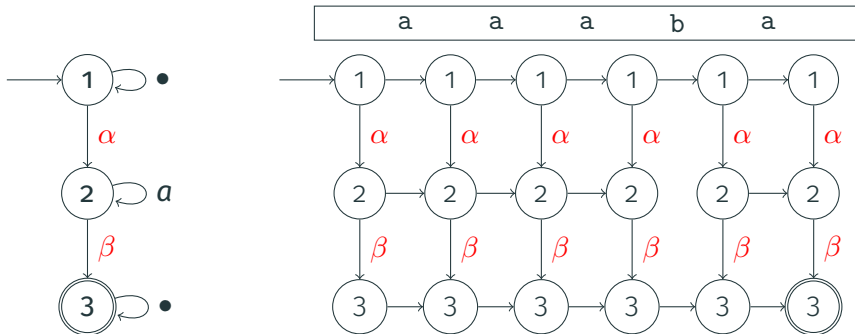
a	a	a	b	a
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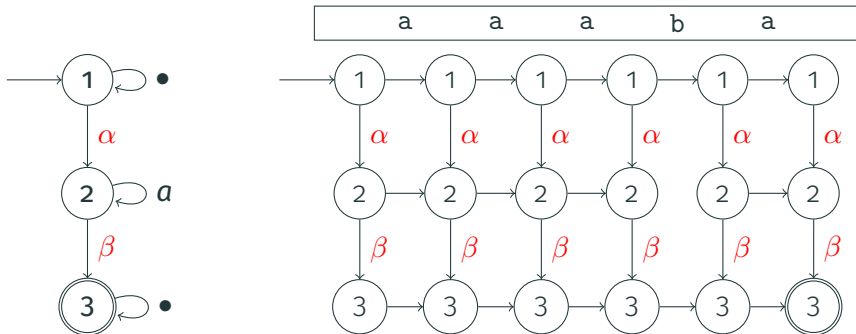
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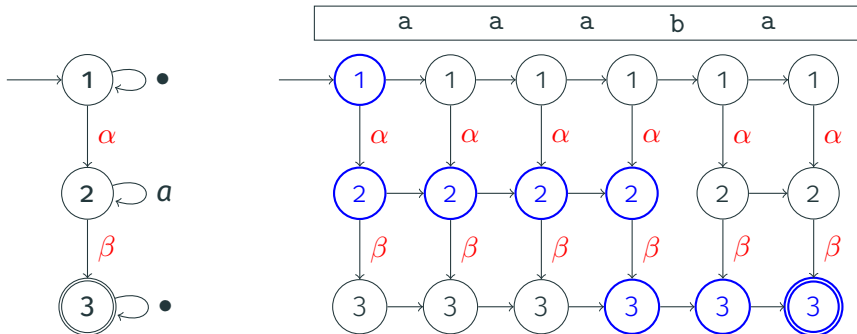


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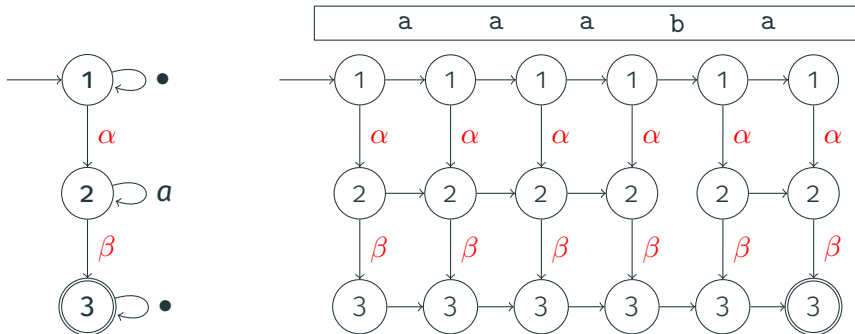


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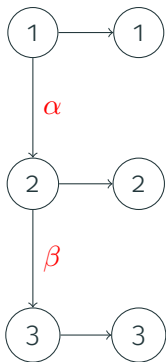
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→ **Challenge:** Enumerate paths but avoid **duplicate matches**
and do not **waste time** to ensure constant delay

Proof idea: on-the-fly computation to avoid duplicates

i $i + 1$

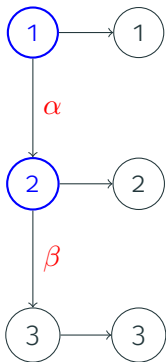
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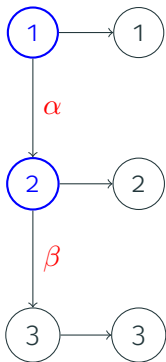
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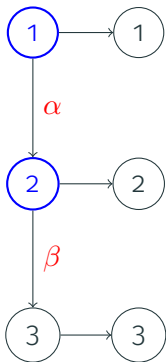
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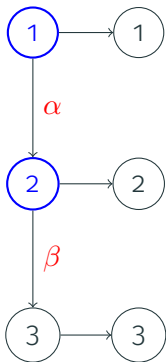
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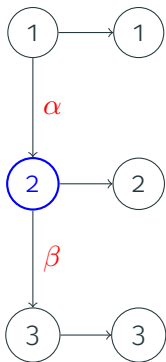
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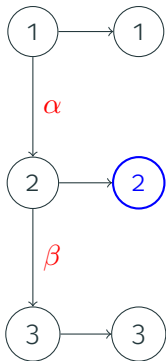
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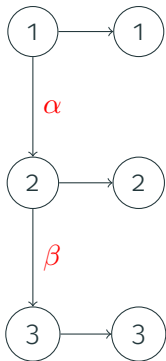
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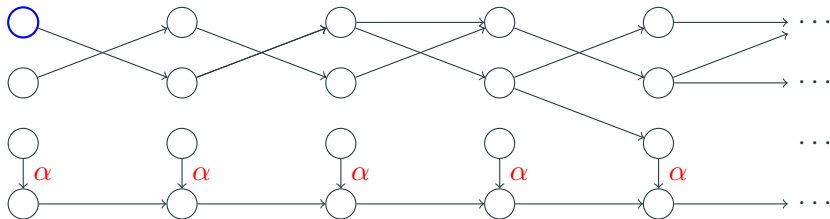
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- We must have **preprocessed** the DAG to make sure that we can always finish the run

Proof idea: jump pointers to save time

- **Issue:** When we can't assign variables, we do not make **progress**

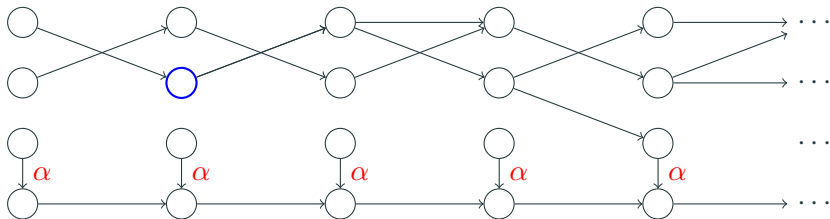
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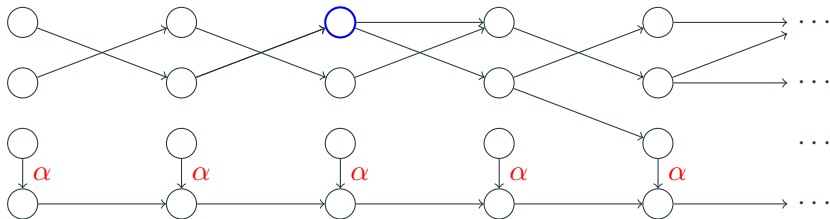
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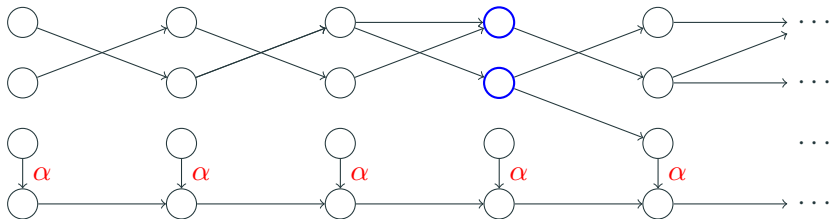
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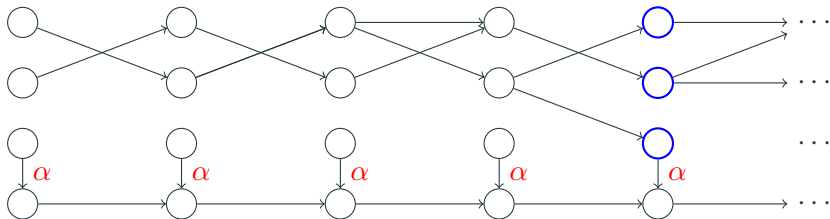
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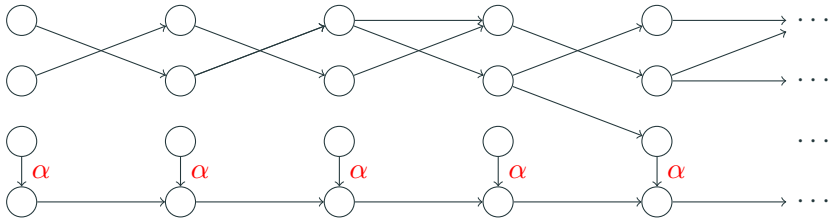
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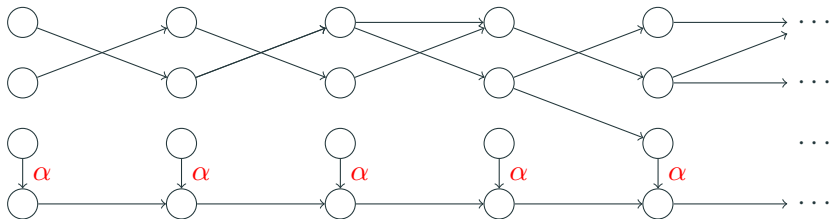
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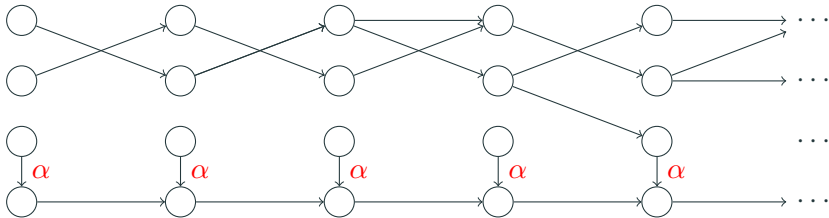
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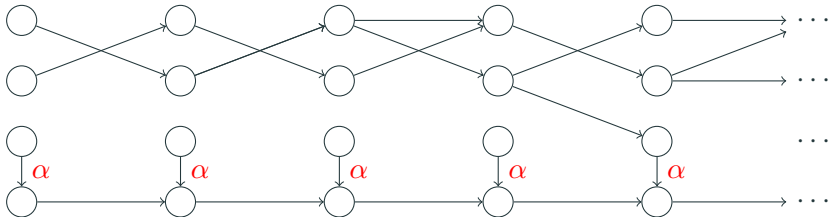
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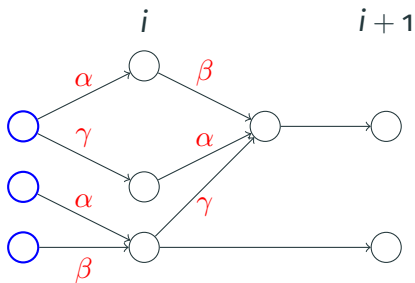
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 - Compute at each position i the **transitive closure** to all positions j such that j is the next position of some state at i (there are $\leq |A|$)

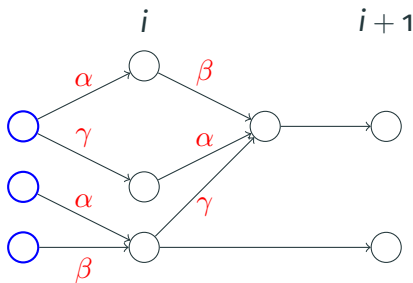
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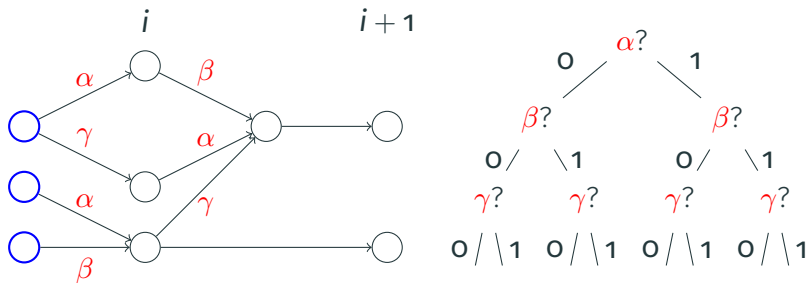
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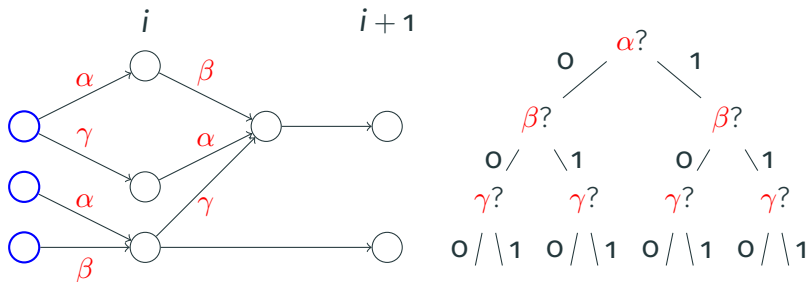
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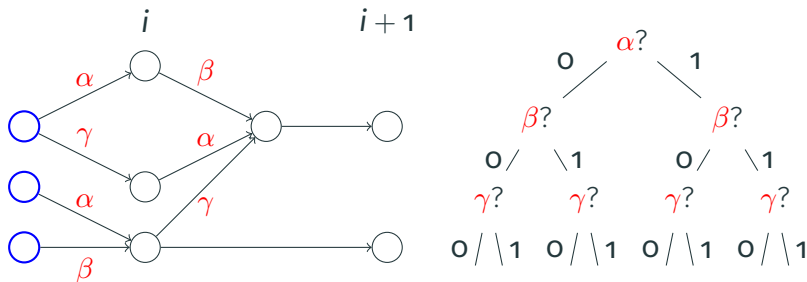
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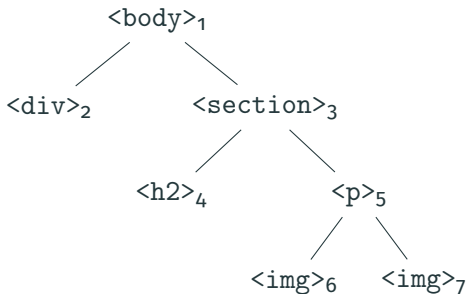


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Extension: From Text to Trees

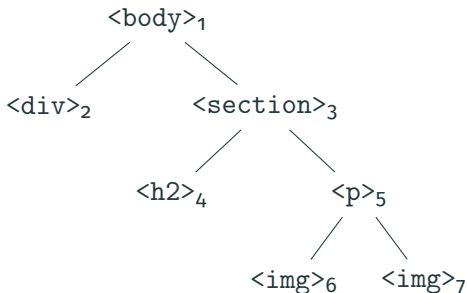
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- The **data** T is no longer **text** but is now a **tree**:



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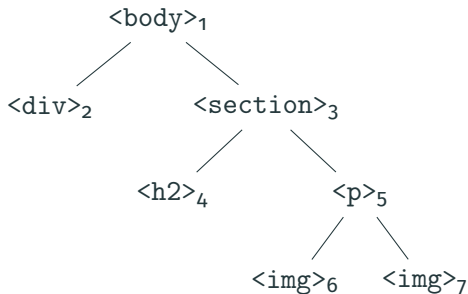
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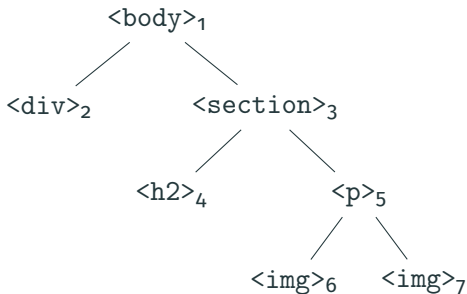
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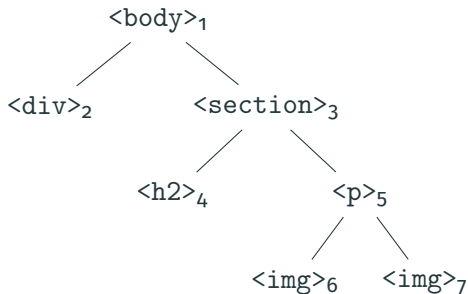
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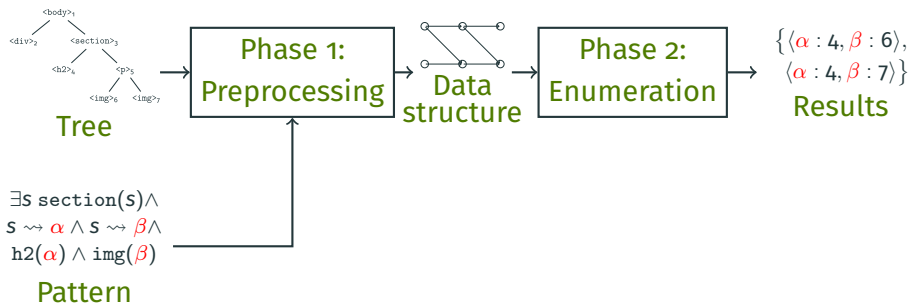
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 - We **conjecture** the following bounds for this task (ongoing work):

Conjecture

- Preprocessing **linear** in T (data) and **polynomial** in A and T (combined)
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Proof idea for trees: structure

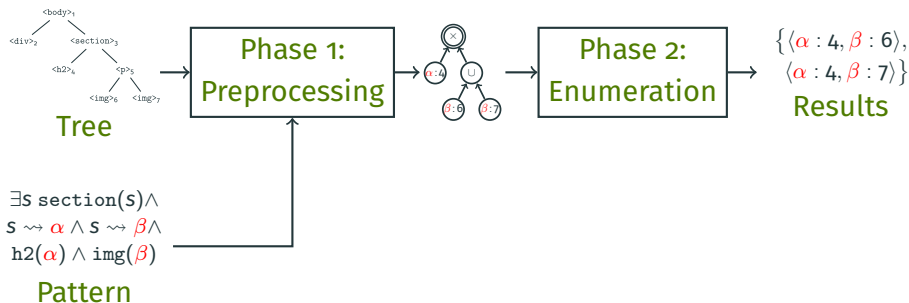
Similar structure to the previous proof, but with a **circuit**:



Proof idea for trees: structure

Similar structure to the previous proof, but with a **circuit**:

- **Preprocessing:** Compute a **circuit representation** of the answers
- **Enumeration:** Apply a **generic algorithm** on the circuit



Proof idea for trees: set circuits

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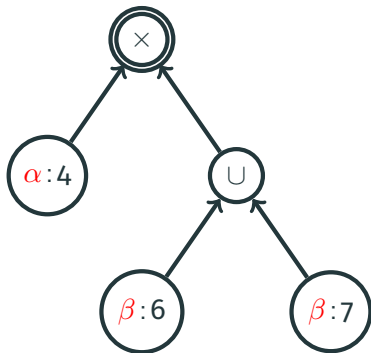
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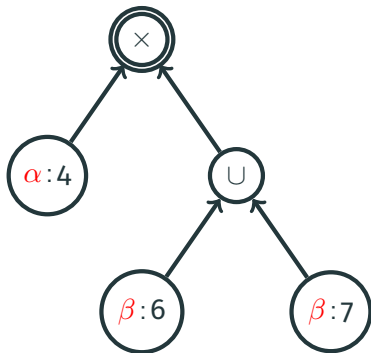
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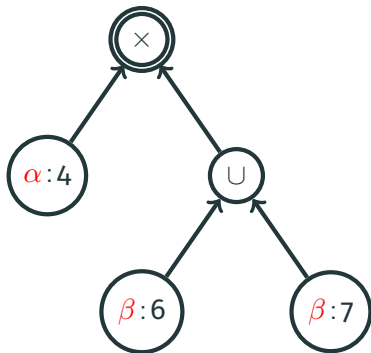
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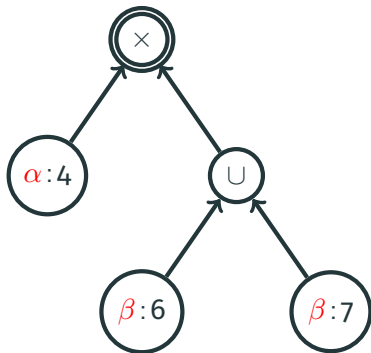
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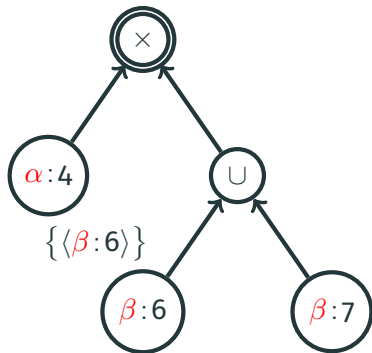
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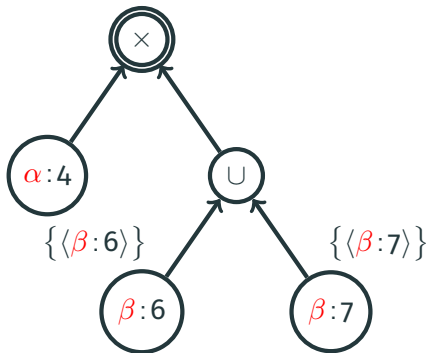
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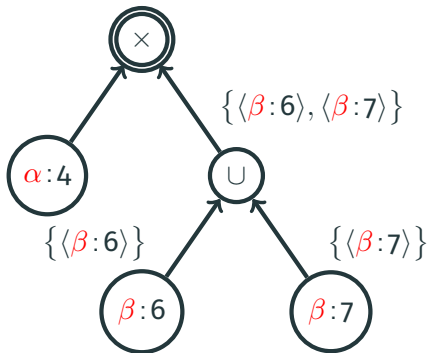
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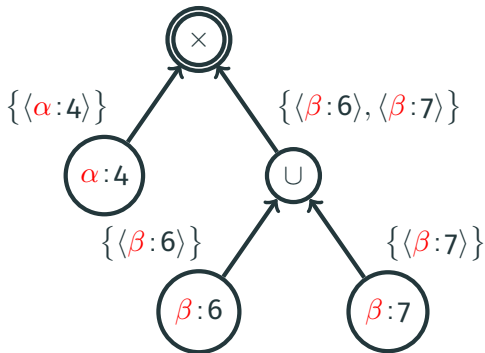
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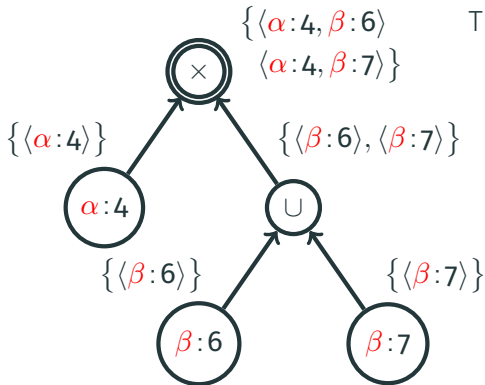
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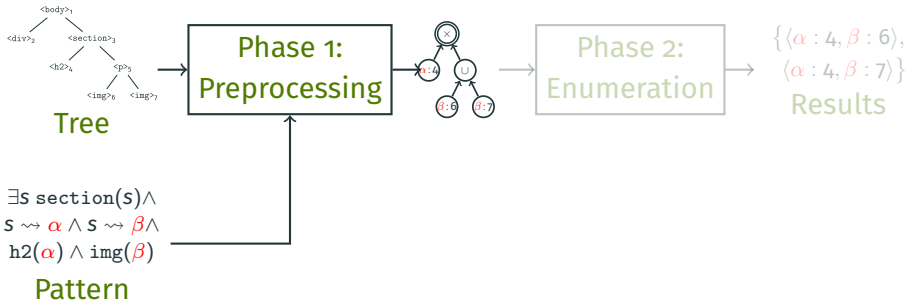
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Proof idea for trees: set circuit construction



Theorem

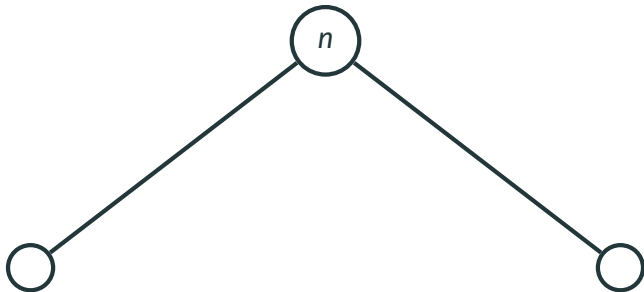
For any **tree automaton** A with capture variables $\alpha_1, \dots, \alpha_k$, given a **tree** T , we can build in $O(|T| \times |A|)$ a **set circuit** capturing exactly the set of tuples $\{\langle \alpha_1 : n_1, \dots, \alpha_k : n_k \rangle$ in the output of A on T

Proof idea for trees: set circuit construction (details)

- **Automaton:** “Select all node pairs (α, β) ”
- **States:** $\{\emptyset, \alpha, \beta, \alpha\beta\}$

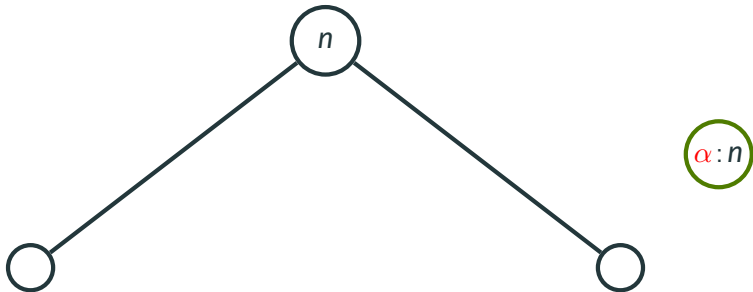
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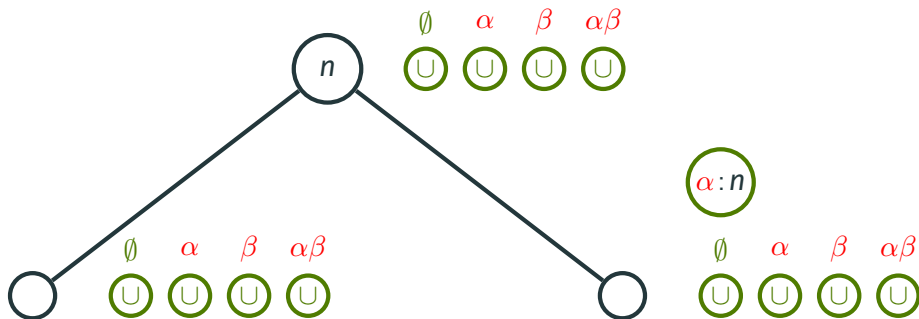
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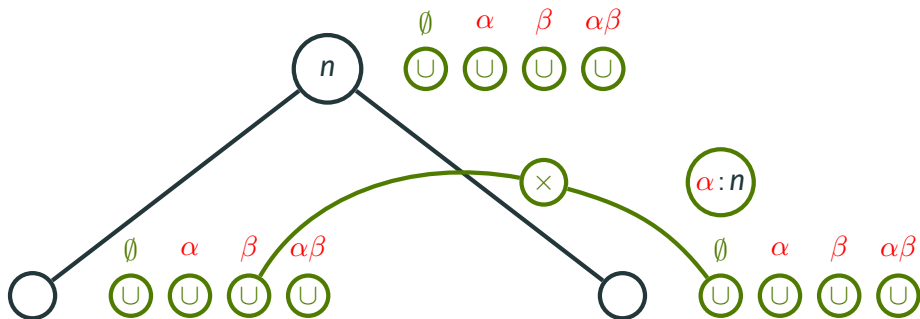
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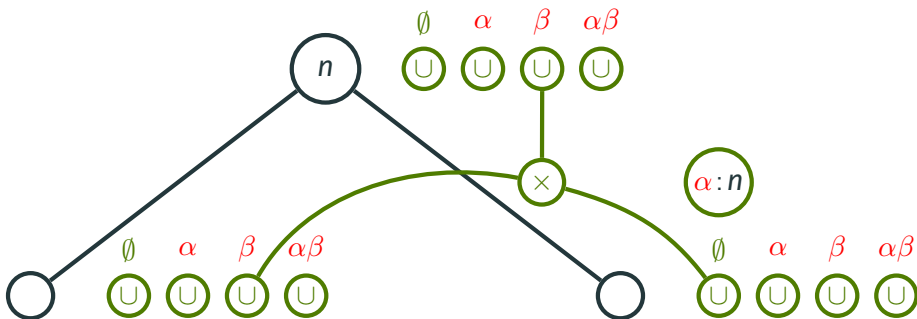
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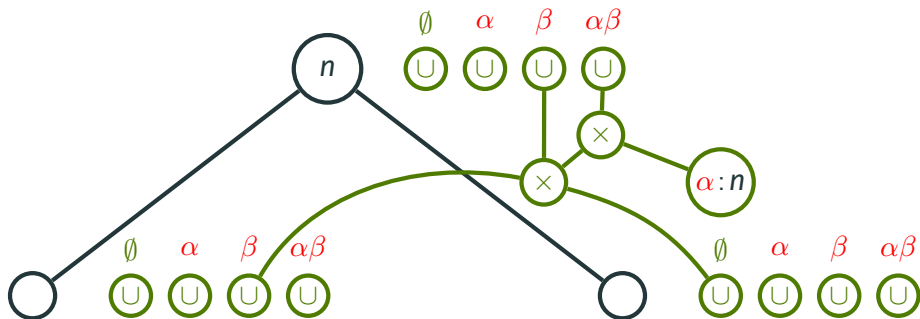
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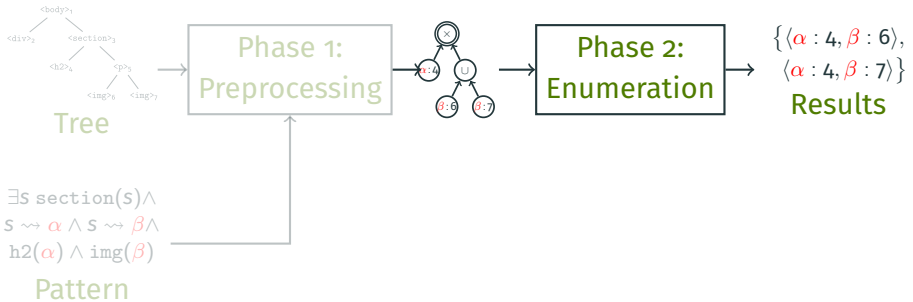


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Proof idea for trees: enumeration on set circuits



Theorem

Given a set circuit *satisfying some conditions*, we can enumerate all tuples that it captures with linear preprocessing and constant delay

E.g., for $\{ \langle \alpha : 4, \beta : 6 \rangle, \langle \alpha : 4, \beta : 7 \rangle \}$: enumerate $\langle \alpha : 4, \beta : 6 \rangle$ then $\langle \alpha : 4, \beta : 7 \rangle$

Proof idea for trees: general enumeration approach

- Enumerate the set $T(g)$ captured by each gate g
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U-gate

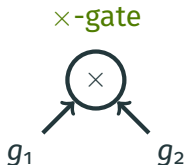
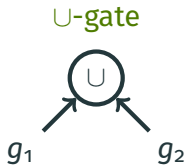


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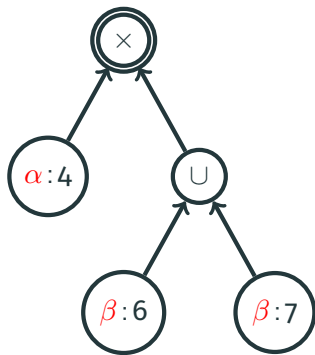


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Lexicographic product:
for every t_1 in $T(g_1)$:
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Proof idea for trees: circuit conditions

Enumeration relies on some **conditions** on the input circuit (d-DNNF):



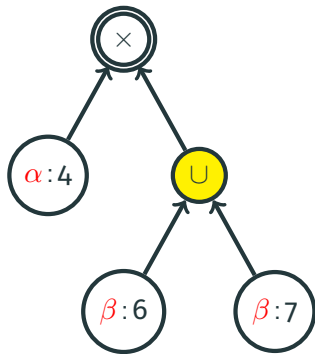
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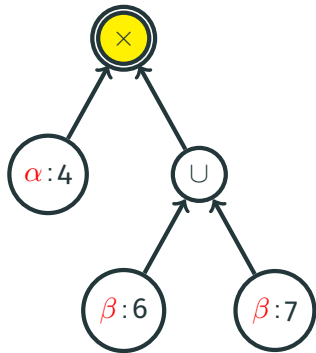
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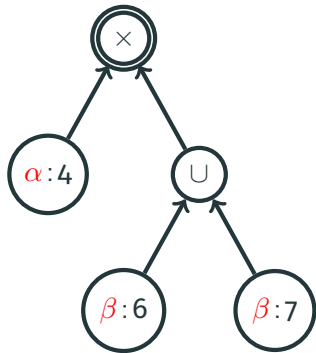
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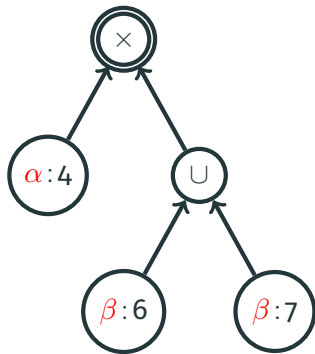
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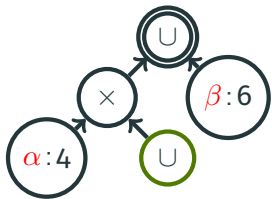
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- Our circuit satisfies these thanks to **automaton determinism**

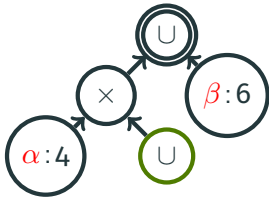


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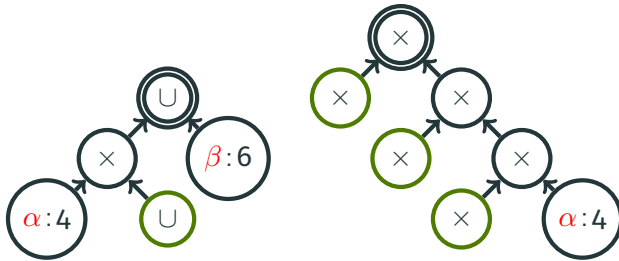
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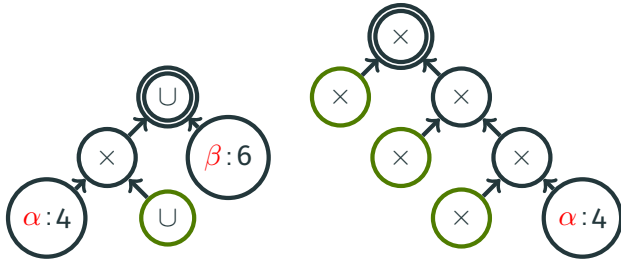
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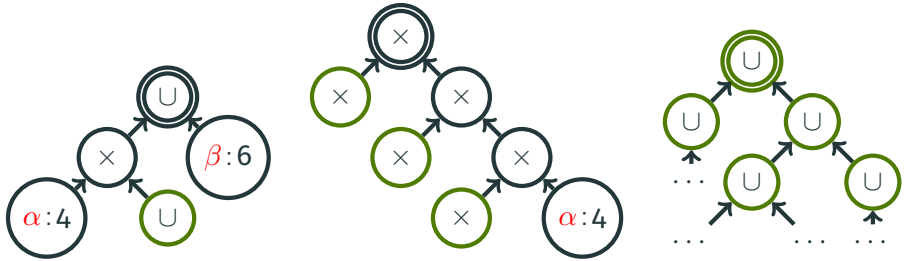
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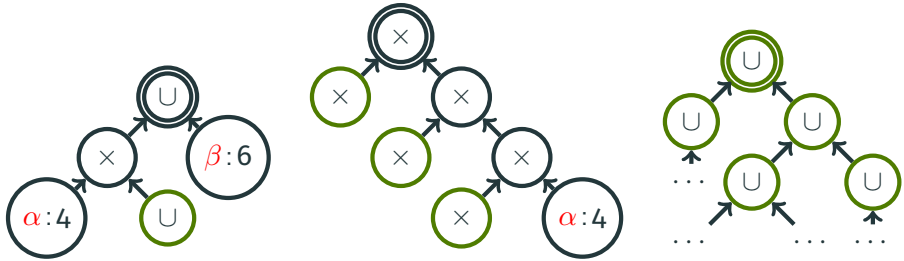
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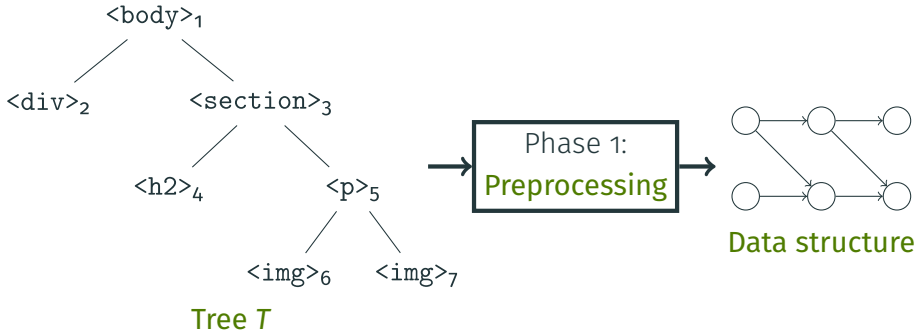
Proof idea for trees: enumeration subtleties



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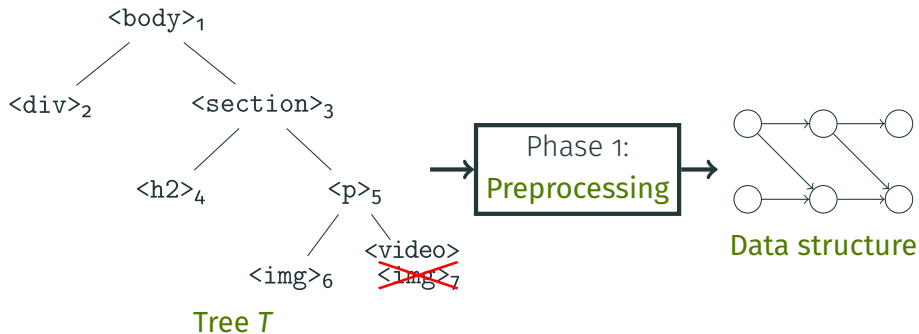
Extension: Handling Updates

Updates



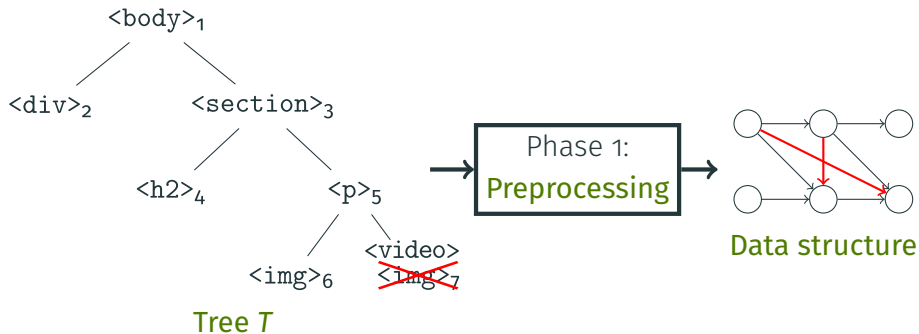
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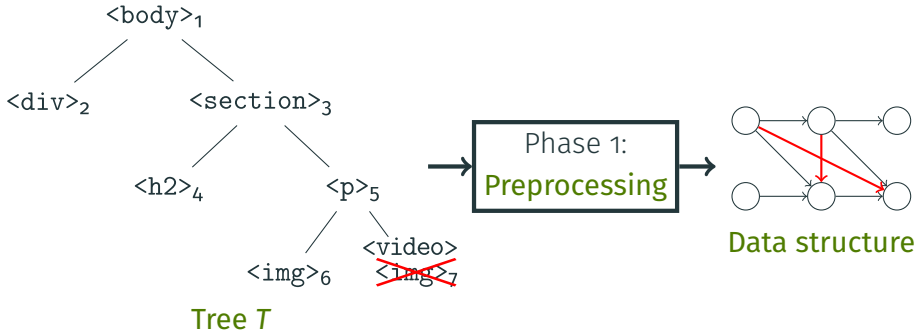
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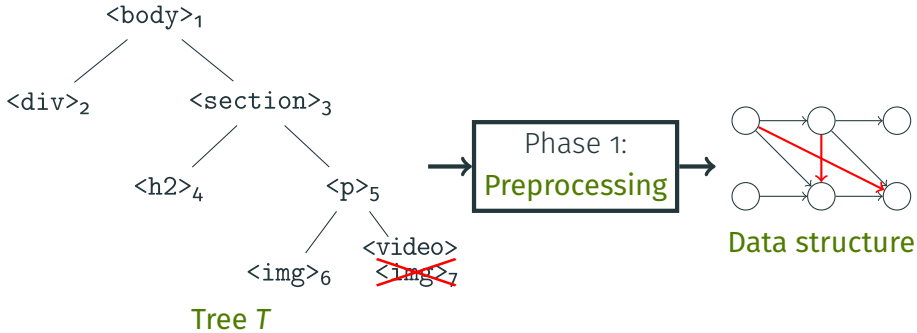
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- Can we **do better**?

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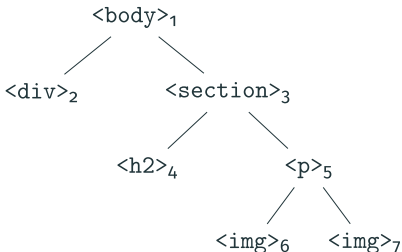
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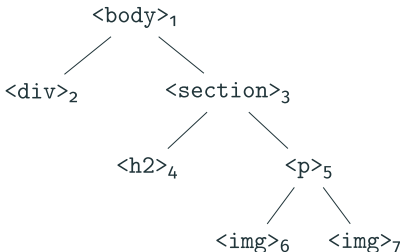
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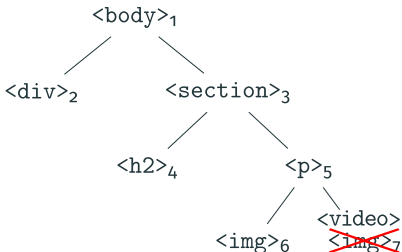
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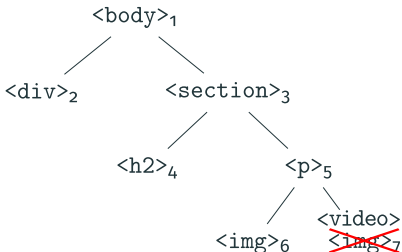
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- Remaining **open questions**:
 - Does this hold for more **general updates** (insert/delete, etc.)?
 - Can we also achieve **tractable combined complexity**?

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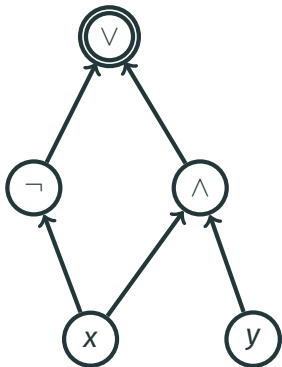
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Boolean circuits



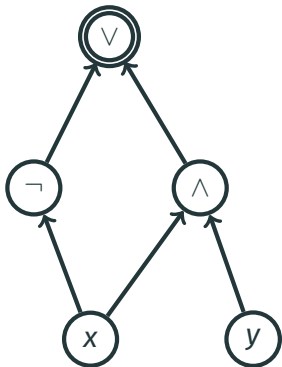
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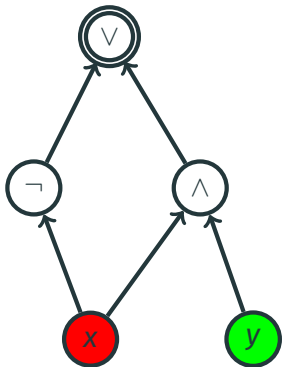
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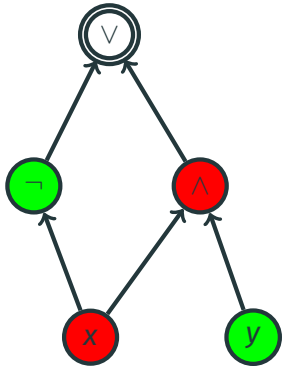
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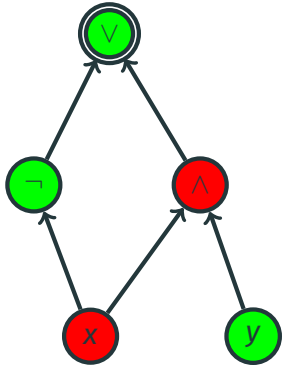
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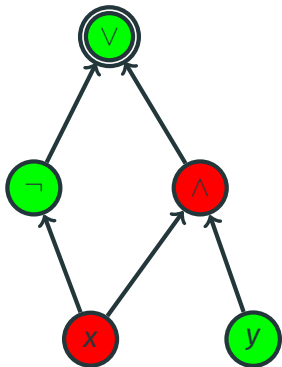
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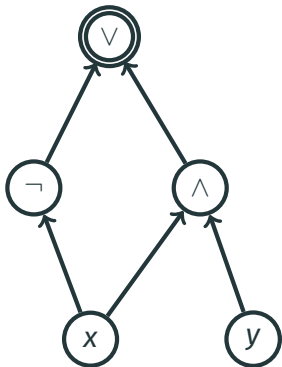
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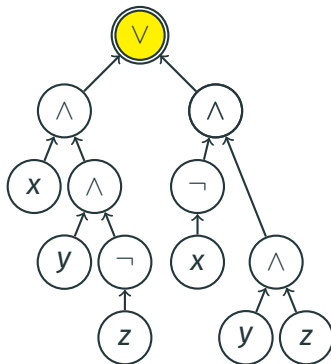
Our task: Enumerate all **satisfying assignments** of an input circuit

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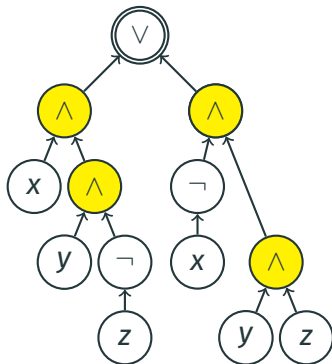
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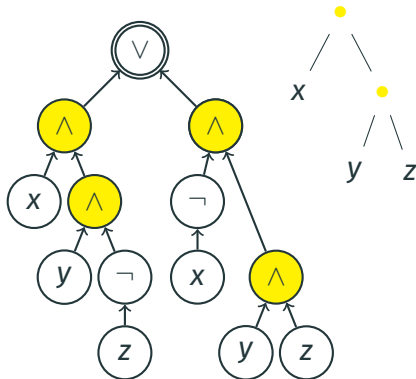
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v-tree: \wedge -gates follow a **tree** on the variables



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Subtleties: Must *complete* to a set circuit; memory usage problems

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Thanks for your attention!

References i

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To appear.